

## Please note that Cypress is an Infineon Technologies Company.

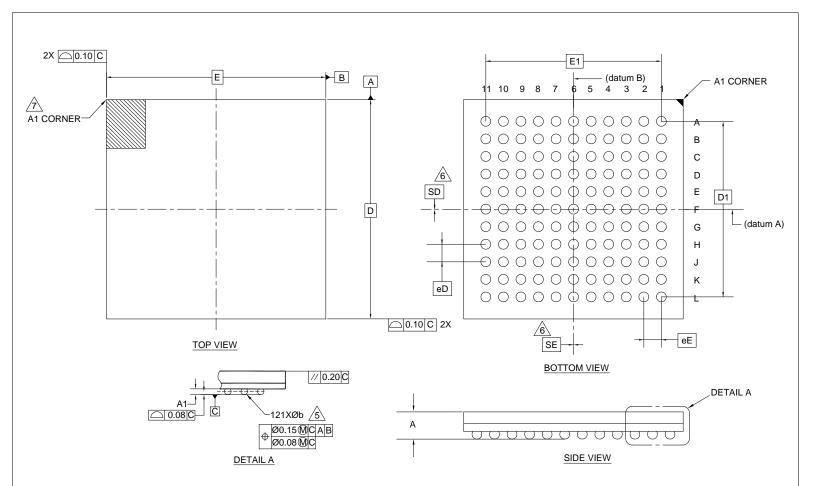
The document following this cover page is marked as "Cypress" document as this is the company that originally developed the product. Please note that Infineon will continue to offer the product to new and existing customers as part of the Infineon product portfolio.

## Continuity of document content

The fact that Infineon offers the following product as part of the Infineon product portfolio does not lead to any changes to this document. Future revisions will occur when appropriate, and any changes will be set out on the document history page.

## Continuity of ordering part numbers

Infineon continues to support existing part numbers. Please continue to use the ordering part numbers listed in the datasheet for ordering.



## NOTES:

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2. SOLDER BALL POSITION DESIGNATION PER JEP95, SECTION 3, SPP-020.
- 3. "e" REPRESENTS THE SOLDER BALL GRID PITCH.
- 4. SYMBOL "MD" IS THE BALL MATRIX SIZE IN THE "D" DIRECTION.
  SYMBOL "ME" IS THE BALL MATRIX SIZE IN THE "E" DIRECTION.
  N IS THE NUMBER OF POPULATED SOLDER BALL POSITIONS FOR MATRIX SIZE MD X ME.
- 5. DIMENSION "b" IS MEASURED AT THE MAXIMUM BALL DIAMETER IN A PLANE PARALLEL TO DATUM C.
- 6. "SD" AND "SE" ARE MEASURED WITH RESPECT TO DATUMS A AND B AND DEFINE THE POSITION OF THE CENTER SOLDER BALL IN THE OUTER ROW. WHEN THERE IS AN ODD NUMBER OF SOLDER BALLS IN THE OUTER ROW, "SD" OR "SE" = 0.
  - WHEN THERE IS AN EVEN NUMBER OF SOLDER BALLS IN THE OUTER ROW, "SD" = eD/2 AND "SE" = eE/2.
- A1 CORNER TO BE IDENTIFIED BY CHAMFER, LASER OR INK MARK METALIZED MARK, INDENTATION OR OTHER MEANS.
- 8. "+" INDICATES THE THEORETICAL CENTER OF DEPOPULATED SOLDER BALLS.

CYPRESS

							Co	ompany Confidential		
								TITLE PACKAGE OUTLINE, 121 BALL FBGA		
THIS DRAWING CONTAINS INFORMATION WHICH IS THE PROPRIETARY PROPERTY OF CYPRESS SEMICONDUCTOR CORPORATION. THIS DRAWING IS RECEIVED IN CONFIDENCE AND ITS CONTENTS MAY NOT BE DISCLOSED WITHOUT WRITTEN CONSENT OF CYPRESS SEMICONDUCTOR CORPORATION.	PACKAGE CODE(S)	BK0AA	FBI121	T4A121	DRAWN BY	DATE	10.0710.071.4	21/14A121		
					KOTA	28-JUN-17	SPEC NO.	1-54471	REV	
					APPROVED BY	DATE		1-34471	^ <b>^⊢</b>	
					GSHN	28-JUN-17	SCALE : TO FIT		SHEET 1 OF 2	

SYMBOL	DIMENSIONS						
	MIN.	MIN. NOM.					
А	-	-	1.20				
A1	0.15	-	-				
D	10.00 BSC						
E	10.00 BSC						
D1	8.00 BSC						
E1	8.00 BSC						
MD	11						
ME	11						
Ν	121						
Ø b	0.25	0.30	0.35				
eD	0.80 BSC						
eE	0.80 BSC						
SD	0.00						
SE	0.00						

	REVISIONS						
Rev	ECN No.	Orig. of change	Reason for Revision				
**	2738843	MLA	NEW RELEASE				
*A	2846357	MLA	CHANGE TO STANDARD TEMPLATE				
*В	2964543	MLA	REVISE BALL HEIGHT DIMENSION AND ADD TOLERANCE TO PACKAGE HEIGHT				
*C	3260577	MLA	REVISE BALL HEIGHT TO MATCH CML AND AMKOR CAPABILITY. ADD PKG WEIGHT				
*D	3722826	MLA	CHANGED PKG WEIGHT FROM "0.2gr" TO "REFER TO PMDD SPEC"				
*E	5112315	КОТА	ADD PACKAGE FBI121 AND T4A121.				
*F	5788956	КОТА	CHANGED PACKAGE CODE FROM BK121A TO BK0AA.				

								PRESS	
					DRAWN BY	DATE		E OUTLINE, 121 B 2 MM BK0AA/FBI12	
THIS DRAWING CONTAINS INFORMATION WHICH IS THE PROPRIETARY PROPERTY OF CYPRESS SEMICONDUCTOR CORPORATION. THIS DRAWING IS RECEIVED IN CONFIDENCE AND ITS CONTENTS MAY NOT BE DISCLOSED WITHOUT WRITTEN CONSENT OF CYPRESS SEMICONDUCTOR CORPORATION.	PACKAGE CODE(S)	BK0AA	FBI121	T4A121	KOTA APPROVED BY	28-JUN-17 DATE	<sup>SPEC NO.</sup> 001-54471		*F
MAY NOT BE DISCLOSED WITHOUT WRITTEN CONSENT OF CIPRESS SEMICONDUCTOR CORPORATION.					GSHN	28-JUN-17	SCALE : TO FIT		SHEET 2 OF 2